

DECLARATION

I, Keita NOGUCHI, a national of Japan, c/o Central Research Laboratory of Hitachi, Ltd. of 1-280, Higashikoigakubo, Kokubunji-shi, Tokyo, Japan, do hereby solemnly and sincerely declare:

- 1) THAT I am well acquainted with the Japanese language and English language, and
- 2) THAT the attached is a full, true and faithful translation into the English language made by me of Japanese Patent Application No. 1995-126405 filed on May 25, 1995

The undersigned declares further that all statements made herein of his own knowledge are true and that all statements made on information and belief are believed to be true ; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Signed this 14th day of March , 2005



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[Document Name] Application for patent

[Reference No.] 195002331

[Application Date] May 25, 1995

[Destination] Commissioner, Patent Office

[International Patent Classification] H01L 21/60

[Title of the Invention] SEMICONDUCTOR DEVICE AND
METHOD OF MANUFACTURE THEREOF

[Number of Claims] 9

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[Priority Claim Based on an Earlier Application]

[Application No.] H06-316444

[Application Date] December 20, 1994

[Designation of Charge]

[Method for Prepayment] Prepaid

[Ledger No. for Prepayment] 014579

[Amount of Payment] 21,000

[List of Document Submitted]

[Object Name] Specification 1

[Object Name] Drawings 1

[Object Name] Abstract 1

[Number of Power of Attorney] 9003109

[Proof] Required

[Name of Document] SPECIFICATION

[Title of the Invention] SEMICONDUCTOR DEVICE AND METHOD
OF MANUFACTURE THEREOF

[Claims]

[Claim 1] A semiconductor device in which a semiconductor pellet is mounted on a pellet mounting area of a main surface of a base substrate, and first electrode pads mounted on a rear surface of the base substrate are electrically connected to external terminals arranged on the main surface of the semiconductor pellet,

wherein the base substrate is formed of a rigid substrate, the first electrode pads of the base substrate are electrically connected to the second electrode pads arranged on its reverse side, the semiconductor pellet is mounted on the pellet mounting area of the main surface of the base substrate with its main surface downward, and the external terminals of the semiconductor pellet and the second electrode pads of the base substrate are electrically interconnected through bonding wires passing through slits formed in the base substrate.

[Claim 2] A semiconductor device according to claim 1, wherein the slits are formed along directions of rows of a plurality of the external terminals that are arranged on the main surface of the semiconductor pellet, and the slits are located on the external terminals of the

semiconductor pellet.

[Claim 3] A semiconductor device according to claims 1 or 2, wherein the second electrode pads of the base substrate, are mounted in both areas on the rear surface of the base substrate divided by the slits.

[Claim 4] A semiconductor device according to claim 3, wherein a power supply is supplied to the second electrode pads arranged on one of both areas of the rear surface on the base substrate divided by the slits, and a signal is applied to the second electrode pads arranged on the other of both areas of the rear surface of the base substrate divided by the slits.

[Claim 5] A semiconductor device according to any one of claims 1 to 4, wherein the rear surface of the semiconductor pellet is exposed from a resin sealing body which covers a peripheral area of the main surface of the base substrate.

[Claim 6] A semiconductor device according to any one of claims 1 to 5, wherein the bonding wires are sealed with the resin sealing body.

[Claim 7] A semiconductor device according to any one of claims 1 to 6, wherein bump electrodes are electrically and mechanically connected to the surfaces of the first electrode pads on the base substrate.

[Claim 8] A method of manufacturing a semiconductor

device in which a semiconductor pellet is mounted on a pellet mounting area of a main surface of a base substrate, and first electrode pads mounted on a rear surface of the base substrate are electrically connected to external terminals arranged on the main surface of the semiconductor pellet,

the method comprising:

a step of mounting the semiconductor pellet, with its main surface downward, on the pellet mounting area of the main surface of a rigid base substrate, and

a step of electrically connecting the external terminal of the semiconductor pellet and second electrode pads electrically connected to the first electrode pads of the base substrate and arranged on the rear surface of the base substrate are electrically interconnected through bonding wires passing through slits formed on the base substrate.

[Claim 9] A method of manufacturing a semiconductor device according to claim 8, further comprising a step of forming by transfer molding a resin sealing body that covers a peripheral area of the main surface of the base substrate and seals the bonding wires, after the step of electrical connection through bonding wires.

[Detailed description of the Invention]

[0001]

[Technical Field]

The present invention relates to a semiconductor device and a method of manufacture thereof and more particularly to a technology effectively applied to a semiconductor device and a method of manufacture thereof, the device having a structure in which a semiconductor pellet is mounted on a pellet mounting area on the main surface of a base substrate and in which first electrode pads on the back of the base substrate are electrically connected to external terminals on the main surface of the semiconductor pellet.

[0002]

[Background Art]

A semiconductor device with a ball grid array (BGA) structure has been introduced as a semiconductor device having a high level of integration in the Nikkei Electronics, Feb. 28, 1994, pp. 111-117, published by Nikkei McGraw-Hill. The BGA structure of such as semiconductor device, as shown in FIG. 16 (cross section of an essential part), has a semiconductor pellet 2 mounted on a pellet mounting area of the main surface of the base substrate 1 and a plurality of bump electrodes 4 arranged in grid on the back of the base substrate 1

opposite the main surface.

[0003]

The base substrate 1 may be made from, for example, a printed wiring board of two-layer wiring structure. A plurality of second electrode pads 1A are arranged in a peripheral area of the main surface of the base substrate 1 (around the pellet mounting area), while a plurality of first electrode pads 1B are arranged on the back of the base substrate 1 opposite the main surface. The second electrode pads 1A are electrically connected to through-hole conductors 1C via conductors 1A₁ arranged on the main surface of the base substrate 1. The first electrode pads 1B are electrically connected to the through-hole conductors 1C via conductors 1B₁ arranged on the back of the base substrate 1.

[0004]

The semiconductor pellet 2, for example, may comprise mainly a semiconductor substrate 2B of single-crystal silicon. On the main surface of the semiconductor substrate 2B (device forming surface) is formed a logic circuit system, a memory circuit system or a combination of these systems. A plurality of external terminals (bonding pads) 2A are arranged on the main surface of the semiconductor substrate 2B. The external terminals 2A are formed in the top of the interconnect layers formed on the

main surface of the semiconductor substrate 2B.

[0005]

The external terminals 2A on the semiconductor pellet 2 are electrically connected to the second electrode pads 1A on the main surface of the base substrate 1 through bonding wires 6. In other words, the external terminals 2A on the semiconductor pellet 2 are electrically connected to the first electrode pads 1B through the bonding wires 3, second electrode pads 1A, conductors 1A₁, through-hole conductors 1C and conductors 1B₁.

[0006]

The semiconductor pellet 29 the bonding wires 6, etc. are sealed with a resin sealing body 7 formed on the main surface of the base substrate 1. The resin sealing body 7 is formed by transfer molding.

[0007]

The bump electrodes 4 are electrically and mechanically connected to the surfaces of the first electrode pads 1B on the base substrate 1. The bump electrodes 4 may be formed from an alloy material, such as Pb-Sn.

[0008]

The semiconductor device of such a BGA structure is mounted on a mounting surface of a mounting board, with

the bump electrodes 4 electrically and mechanically connected to electrode pads arranged on the mounting surface of the mounting board.

[0009]

Another example of semiconductor device having a high circuit density is disclosed in U.S. Pat. Ser. No. 5148265, which shows a semiconductor device in which the base substrate is made from a flexible substrate. In this semiconductor device, the semiconductor pellet is mounted, with its main surface downward, on the pellet mounting area of the main surface of the base substrate made of a flexible substrate, and the external terminals arranged on the main surface of the semiconductor pellet are electrically connected to the second electrode pads arranged on the back of the base substrate through the bonding wires. The second electrode pads on the base substrate are electrically connected to the first electrode pads on the back of the base substrate through conductors that are also arranged on the back. Bump electrodes are electrically and mechanically connected to the surfaces of the first electrode pads.

[0010]

The semiconductor device of the above construction is mounted on the mounting surface of a mounting board, with its bump electrodes electrically and mechanically

connected to the electrode pads arranged on the mounting surface of the mounting board.

[0011]

[Problems to be Solved by the Invention]

(1) In the semiconductor device with the BGA structure, as shown in FIG. 16, the second electrode pads 1A arranged on the main surface of the base substrate 1 are electrically connected through the through-hole conductors 1C to the first electrode pads 1B arranged on the back of the base substrate 1. The through-hole conductors 1C comprises a hole area formed within a through-hole in the base substrate 1 and a land area (fringe portion) formed on the main surface and back surface of the base substrate 1. The inner diameter of the through-hole may be around ϕ 0.3 mm and the outer diameter of the land area of the through-hole conductor 1C may be about ϕ 0.6 mm. The inner diameter of the through-hole and the outer diameter of the land area of the through-hole conductor 1C are set large compared to the widths of the conductors 1A, electrically connecting the second electrode pads 1A with the through-hole conductors 1C and also compared to the widths of the conductors 1B, electrically connecting the first electrode pads 1B and the through-hole conductors 1C.

[0012]

On the other hand, the circuit systems formed on the typical semiconductor pellets 2 have tended to grow in their level of integration. With enhanced integration of the circuit system, the number of external terminals 2A of the semiconductor pellet 2 and the number of second electrode pads 1A of the base substrate 1 increase. That is, the number of through-hole conductors 1C electrically connecting the second electrode pads 1A with the first electrode pads 1B increases as the integration of the circuit system are enhanced. Hence, there has been a problem that the external size of the base substrate 1 increases in accordance with the number of the through-hole conductors 1C increased, which in turn increases the size of the semiconductor device as a whole.

[0013]

(2) In a semiconductor device with the BGA structure, as the number of the through-hole conductors 1C increases, they are positioned outwardly away from the semiconductor pellet 2. This inevitably extends the length of the conductors 1A₁ electrically connecting the second electrode pads 1A with the through-hole conductors 1C and the length of the conductors 1B₁, electrically connecting the first electrode pads 1B with the through-hole conductors 1C. This, in turn, increases inductance and reduces the operating speed of the semiconductor device.

[0014]

(3) In the semiconductor device using a flexible substrate for the base substrate, the flexible substrate may for example be formed of a polyester film or polyimide film. This flexible substrate has a small Young's modulus and is soft (low hardness) compared with a rigid substrate made of a glass fiber impregnated with such as epoxy resin or polyimide resin. Therefore, when the external terminals arranged on the main surface of the semiconductor pellet are electrically connected with the second electrode pads arranged on the back of the base substrate through bonding wires, the bonding force applied to the second electrode pads is absorbed by the base substrate, preventing the bonding force and ultrasonic vibrations from being transmitted to the second electrode pads effectively. This gives rise to an apprehension that the connection strength between the bonding wires and the second electrode pads may decrease, leading to connection failures of bonding wires and reduced electric reliability of the semiconductor device.

[0015]

(4) In semiconductor devices that use a flexible substrate for the base substrate, the flexible substrate has a large thermal expansion coefficient in the planar direction and a small Young's modulus (small rigidity),

which means that it is easy to bend, compared with the rigid substrate. Therefore, when the semiconductor device is mounted on the mounting surface of the mounting board, the reflow heat used during the process of mounting causes deformations to the base substrate, such as warping and twisting, which in turn reduces the flatness of the back of the base substrate with respect to the mounting surface of the mounting board, thereby lowering the mounting precision of the semiconductor device.

[0016]

An object of this invention is to provide a technology that allows a reduction in the size of the semiconductor device.

[0017]

Another object of this invention is to provide a technology that allows an increase in the operating speed of the semiconductor device.

[0018]

Still another object of this invention is to provide a technology that can enhance electric reliability of the semiconductor device.

[0019]

A further object of this invention is to provide a technology that can enhance mounting precision of the semiconductor device.

[0020]

A further object of this invention is to provide a manufacturing technology for the semiconductor device that can accomplish the above objectives.

[0021]

These and other objects and novel features of this invention will become apparent from the following description of this specification and the accompanying drawings.

[0022]

[Means for Solving the Problems]

Representative aspects of the invention disclosed in this application may be briefly summarized as follows.

[0023]

(1) In a semiconductor device in which a semiconductor pellet is mounted on a pellet mounting area of the main surface of a base substrate, and first electrode pads mounted on the rear surface of the base substrate are electrically connected to external terminals arranged on the main surface of the semiconductor pellet, the base substrate is formed of a rigid substrate, the first electrode pads of the base substrate are electrically connected to the second electrode pads arranged on its reverse side, the semiconductor pellet is mounted on the pellet mounting area of the main surface of

the base substrate with its main surface downward, and the external terminals of the semiconductor pellet and the second electrode pads of the base substrate are electrically interconnected through bonding wires passing through slits formed in the base substrate.

[0024]

(2) In a method of manufacturing a semiconductor device in which a semiconductor pellet is mounted on a pellet mounting area of the main surface of a base substrate, and first electrode pads mounted on the rear surface of the base substrate are electrically connected to external terminals arranged on the main surface of the semiconductor pellet, the method comprises a step of mounting the semiconductor pellet, with its main surface downward, on the pellet mounting area of the main surface of a rigid base substrate, and a step of electrically connecting the external terminal of the semiconductor pellet and second electrode pads electrically connected to the first electrode pads of the base substrate and arranged on the rear surface of the base substrate are electrically interconnected through bonding wires passing through slits formed on the base substrate.

[0025]

[Operation]

According to the above means (1) of this invention,

the external terminals of the semiconductor pellet and the first electrode pads of the base substrate can be electrically connected through the bonding wires and the second electrode pads, so it is possible to eliminate the through-hole conductors used to electrically connect the second electrode pads with the first electrode pads in prior structures. This allows the external size of the base substrate to be reduced by an amount corresponding to an area occupied by the through hole conductors (area of land region), thus reducing the size of the semiconductor device as a whole.

[0026]

Further, because the first electrode pads can be put closer to the second electrode pads by an amount corresponding to an area occupied by the through hole conductors, the conductors of the base substrate that electrically connect the second electrode pads with the first electrode pads can be reduced in length. As a result, inductance can be reduced and the operating speed of the semiconductor device increased.

[0027]

Further, the rigid substrate has a higher Young's modulus and is harder than a flexible substrate; therefore, when the external terminals arranged on the main surface of the semiconductor pellet and the second electrode pads

arranged on the back of the base substrate are electrically connected by bonding wires, the bonding force applied to the second electrode pads can be prevented from being absorbed by the base substrate. This assures effective transmission of the bonding force and the ultrasonic vibrations to the second electrode pads. Thus, the connection strength between the bonding wires and the second electrode pads is increased, making it possible to prevent connection failure of the bonding wires and to enhance electric reliability of the semiconductor device.

[0028]

Furthermore, the rigid substrate has a small planar thermal expansion coefficient and a high Young's modulus compared with a flexible substrate, which means the rigid substrate is hard to bend. This prevents the base substrate from being deformed (warped or twisted) due to reflow heat produced during the process of mounting the semiconductor device on the mounting surface of the mounting board. This ensures a sufficient flatness of the back of the base substrate with respect to the mounting surface of the mounting board, thus enhancing the mounting precision of the semiconductor device.

[0029]

According to the above-mentioned means (2) of this invention, the external terminals of the semiconductor

pellet and the first electrode pads of the base substrate are electrically connected through bonding wires and second electrode pads, so the through hole conductors electrically connecting the second electrode pads with the first electrode pads can be eliminated, making it possible to use a base substrate reduced in external size by an amount corresponding to the occupied area of the through hole conductors. This in turn allows the manufacture of reduced-size semiconductor devices.

[0030]

Further, because the external terminals of the semiconductor pellet and the first electrode pads of the base substrate are electrically connected through bonding wires and second electrode pads, the through hole conductors electrically connecting the second electrode pads with the first electrode pads can be eliminated, making it possible to use a base substrate whose conductors electrically connecting the second electrode pads with the first electrode pads are reduced by a length corresponding to the occupied area of the through hole conductors. This in turn allows the manufacture of semiconductor devices with faster operating speeds.

[0031]

The base substrate uses a rigid substrate having a high Young's modulus, which is hard, compared with a

flexible substrate; therefore, when the external terminals arranged on the main surface of the semiconductor pellet and the second electrode pads arranged on the back of the base substrate are electrically connected by bonding wires, the bonding force applied to the second electrode pads can be prevented from being absorbed by the base substrate, ensuring effective transfer of the bonding force and ultrasonic vibrations to the second electrode pads. This enhances the connection strength between the bonding wires and the second electrode pads, allowing the manufacture of the semiconductor device with high electric reliability.

[0032]

Furthermore, the base substrate is used, which is formed of a rigid substrate having a small planar thermal expansion coefficient and a high Young's modulus compared with those of a flexible substrate, which means the rigid substrate is hard to bend. As a result, the rigid base substrate is free from deformations (warping or twisting) due to reflow heat during the process of mounting the semiconductor device on the mounting surface of the mounting board. As a result, a sufficient degree of flatness of the back of the base substrate with respect to the mounting surface of the mounting board can be secured, which in turn allows the manufacture of semiconductor devices with high mounting precision.

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[0033]

[Embodiments]

The construction of this invention is described in the following in conjunction with embodiments that apply this invention to a semiconductor device using the BGA structure.

[0034]

In the drawings used for explaining the embodiments, components with identical functions are given like reference numerals and their explanations are not repeated.

[0035]

(Embodiment 1)

The outline construction of a semiconductor device, as a first embodiment of this invention, that uses the BGA structure is shown in FIG. 1 (plan view of the main surface side), FIG. 2 (cross section taken along the line A--A of FIG. 1), FIG. 3 (enlarged cross section of an essential part of FIG. 2) and FIG. 4 (enlarged plan view showing the back side of an essential part of the semiconductor device with the resin sealing body removed).

[0036]

As shown in FIGS. 1, 2, 3 and 4, the semiconductor device has a semiconductor pellet 2 mounted on a pellet mounting area of the main surface of a base substrate 1, with a plurality of bump electrodes 4 arranged in grid on

the back of the base substrate 1 opposite the main surface.

[0037]

The base substrate 1 may be formed of, for example, a printed circuit board. The printed circuit board may, for example, have a structure in which wiring is formed over the surface of a rigid substrate of glass fiber impregnated with such as epoxy resin, polyimide resin or maleimide resin. In other words, the base substrate 1 is formed of a rigid substrate. The rigid substrate has a high Young's modulus and is hard compared with a flexible substrate made of such as polyester film or polyimide film. The rigid substrate has a small thermal expansion coefficient in a planar direction, a high Young's modulus and is difficult to bend compared with the flexible substrate. For example, the rigid substrate made of a glass fiber impregnated with epoxy resin or polyimide resin has a Young's modulus of around 16 to 22 GPa and a thermal expansion coefficient of about $10 \text{ to } 20 \times 10^{-6}$ [1/ $^{\circ}\text{C}$]. Flexible substrates made of polyester film or polyimide film have a Young's modulus of about 2 - 5 GPa and a thermal expansion coefficient of about $20 \text{ to } 25 \times 10^{-6}$ [1/ $^{\circ}\text{C}$].

[0038]

On the back of the base substrate 1 are formed a plurality of second electrode pads 1A and first electrode

pads 1B, which are electrically interconnected through conductors 1B₁ on the back of the base substrate 1. The second electrode pads 1A, first electrode pads 1B and conductors 1B₁ are formed of a Cu film, for example.

[0039]

On the surfaces of the first electrode pads 1B are formed bump electrodes 4 that are electrically and mechanically connected to them. The bump electrodes 4 may be formed of, for instance, a Pb-Sn alloy.

[0040]

The semiconductor pellet 2 is mounted, with its main surface (underside in FIGS. 2 and 3) downward, on the pellet mounting area of the main surface of the base substrate 1. That is, the semiconductor pellet 2 is mounted by a facedown system on the pellet mounting area of the main surface of the base substrate 1. Interposed between the main surface of the semiconductor pellet 2 and the pellet mounting area of the main surface of the base substrate 1 is an insulating layer 3, which may be formed of a polyimide-, epoxy- or silicon-base low-elasticity resin.

[0041]

The semiconductor pellet 2 may be rectangular on the plane surface and may mainly be comprised of a semiconductor substrate 1B made of, for example, single-

crystal silicon. On the main surface (device forming surface) of the semiconductor substrate 1B are formed a logic circuit system, a memory circuit system or a combination of these systems. Also on the main surface of the semiconductor substrate 1B, a plurality of external terminals (bonding pads) 2A are arranged along the sides of the rectangular surface. The external terminals 2A are formed on the top of interconnect layers on the main surface of the semiconductor substrate 2B. That is, a plurality of the external terminals 2A is arranged in the periphery of the main surface of the semiconductor pellet 2 for each of the sides.

[0042]

The external terminals 2A of the semiconductor pellet 2 and the second electrode pads 1A of the base substrate 1 are electrically interconnected through bonding wires 6 running in slits 5 formed in the base substrate 1. The bonding wires 6 may be of gold (AU), copper (Cu) or aluminum (Al), or may be formed by coating a surface of a metal wire with insulating resin or the like. The bonding wires 6 may be connected by a bonding method that utilizes ultrasonic vibrations in combination with thermo-compression.

[0043]

The slits 5 in the base substrate 1 are formed in

the directions of the rows of the external terminals 2A that are arranged along each side of the main surface of the semiconductor pellet 2. That is, the base substrate 1 of this embodiment has four slits 5, each of which is located above the external terminals 2A of the semiconductor pellet 2.

[0044]

The second electrode pads 1A of the base substrate 1 are placed in both areas of the back of the base substrate 1 divided by the slits 5. The second electrode pads 1A located in one of the areas of the back of the base substrate 1 demarcated by the slits 5 (inside the semiconductor pellet 2) are supplied with a power supply such as an operation voltage (3.3 V for instance) and a reference voltage (0 V for instance). The second electrode pads 1A located in the other area of the back of the base substrate 1 demarcated by the slits 5 (outside the semiconductor pellet 2) receive a signal such as an input/output signal and a control signal.

[0045]

The semiconductor pellet 2 is provided with 100 external terminals 2A on each side at a pitch of about 100 μm , for example. The number of external terminals 2A is increased as the level of integration and the operating speed of the circuit system mounted on the semiconductor

pellet 2 increase.

[0046]

The one area of the back of the base substrate 1 demarcated by the slits 5 is provided with, for example, 50 second electrode pads 1A for each side of the semiconductor pellet 2; and the other area is provided with, for instance, 50 second electrode pads 1A for each side of the semiconductor pellet 2. Because the second electrode pads 1A cannot be made as small as the external terminals 2A of the semiconductor pellet 2, the pitch of the second electrode pads 1A is set wider than that of the external terminals 2A, for instance, at around 200 μm . That is, because the second electrode pads 1A of the base substrate 1 are arranged in two rows for each side of the semiconductor pellet 2, the length of the second electrode pads 1A corresponding to one side of the semiconductor pellet 2 can be made almost equal to that of the external terminals 2A arranged along one side of the semiconductor pellet 2 even if the pitch of the second electrode pads 1A of the base substrate 1 is set to two times that of the external terminals 2A of the semiconductor pellet 2. Furthermore, the second electrode pads 1A of the base substrate 1 can be located at positions facing the corresponding external terminals 2A of the semiconductor pellet 2.

[0047]

The peripheral area of the main surface of the base substrate 1 excluding the pellet mounting area is covered with a resin sealing body 7, which seals the bonding wires 6. That is, the resin sealing body 7 is formed on the main surface side and the back surface side of the base substrate 1. The resin sealing body 7 is made from epoxy resin 7A containing a phenol-base hardener, silicone rubber and filler for reducing stresses.

[0048]

The back of the base substrate 1 facing the main surface of the semiconductor pellet 2 is exposed from the resin sealing body 7 that covers the peripheral area of the base substrate 1.

[0049]

The resin sealing body 7 is formed by the transfer molding that uses a molding die 10 shown in FIG. 5 (cross section of an essential part). The molding die 10 has a cavity 11 defined by an upper die 10A and a lower die 10B, an inflow gate 13 connected to the cavity 11, and, though not shown, a pot and a runner. The pot communicates with the cavity 11 through the runner and the inflow gate 13.

[0050]

The cavity 11 comprises a recess 11A formed in the upper die 10A and a recess 11B formed in the lower die 10B.

The resin 7A is supplied into the recess 11A from the pot through the runner and the inflow gate 13. The base substrate 1 is mounted in the recess 11B.

[0051]

The recess 11B is formed with recesses 12, which are located at positions facing the slits 5 of the base substrate 1 mounted in the recess 11B and which extend in the same directions as the slits 5. Placed in the recesses 12 are a part of bonding wires 6 electrically connecting the external terminals 2A of the semiconductor pellet 2 with the second electrode pads 1A of the base substrate 1, and also the second electrode pads 1A of the base substrate 1. The resin 7A is supplied from the recess 11A through the slits 5 of the base substrate 1.

[0052]

Though not shown in FIG. 12, the recesses 12 are provided with a gas vent to prevent generation of voids due to bubbles.

[0053]

Next, the method of manufacturing the above-mentioned semiconductor device is described by referring to FIGS. 6 through FIG. 9.

[0054]

First, a base substrate 1 made of a rigid substrate is prepared. The base substrate 1 includes slits 5 as

well as second electrode pads 1A, first electrode pads 1B and conductors 1B₁ on its back.

[0055]

Next, as shown in FIG. 6 (cross section), the semiconductor pellet 2 is mounted on the pellet mounting area of the main surface of the base substrate 1. The semiconductor pellet 2 is fixed to the pellet mounting area of the main surface of the base substrate 1 through an insulating layer 3.

[0056]

Next, the base substrate 1 is mounted on a bonding stage (heat block) 14 with the semiconductor pellet 2 facing downward. The bonding stage 14 has a recess 14A that accommodates the semiconductor pellet 2. The base substrate 1 and the semiconductor pellet 2 are heated to about 200°C. on the bonding stage 14.

[0057]

Next, as shown in FIG. 7 (cross section of an essential part), the external terminals 2A arranged on the main surface of the semiconductor pellet 2 and the second electrode pads 1A arranged on the back of the base substrate 1 are electrically connected by the bonding wires 6. The bonding wires 6 running in the slits 5 of the base substrate 1 are connected to the external terminals 2A of the semiconductor pellet 2 and to the

second electrode pads 1A of the base substrate 1. The connection of the bonding wires 6 is accomplished by ultrasonic thermo-compression bonding. In this process, the base substrate 1 is made from a rigid substrate with a high Young's modulus compared with the flexible substrate used in conventional structure, so that the bonding force applied to the second electrode pads 1A is prevented from being absorbed by the rigid base substrate 1, thus allowing the bonding force and the ultrasonic vibrations to be transferred effectively to the second electrode pads 1A. Further, because the base substrate 1 is made of a rigid substrate that has a smaller thermal expansion coefficient in the planar direction than that of a flexible substrate and a higher Young's modulus, which means it is hard to bend, and thus it is possible to reduce positional deviations of the second electrode pads 1A and of the external terminals 2A of the semiconductor pellet 2 due to thermal expansion of the base substrate 1.

[0058]

Then, as shown in FIG. 8 (cross section of an essential part), the base substrate 1 and the semiconductor pellet 2 are put in the cavity 11 defined by the upper die 10A and the lower die 10B of the molding die 10, with the base substrate 1 fit in the recess 11B of the cavity 11. A part of the bonding wires 6 and the second

electrode pads 1A of the base substrate 1 are placed in the recesses 12 formed in the recess 11B. The molding die 10 is preheated to around 170°C to 180°C to heighten the fluidity of the resin 7A supplied into the cavity 11. Because the base substrate 1 is made from a rigid substrate with a smaller thermal expansion coefficient in the planar direction than that of the flexible substrate and with a higher Young's modulus, which means the base substrate 1 is harder to bend, the base substrate 1 can be prevented from being deformed (warped or twisted) due to the heating of the molding die 10.

[0059]

Next, resin tablets are charged into the pot of the molding die 10, noting that they are preheated by a heater to lower the viscosity before being charged. The resin tablets in the pot are heated by the molding die 10, further lowering the viscosity.

[0060]

The resin is then pressurized by a plunger of the transfer molding device, forcing the resin 7A from the pot through the runner and the gate 13 into the recess 11A and the recesses 12 of the cavity 11, as shown in FIG. 9 (cross section of an essential part), to cover the peripheral area of the main surface of the base substrate 1, leaving the back of the semiconductor pellet 2 exposed.

In this way, a resin sealing body 7 that seals the bonding wires 6 is formed. The resin 7A is forced into the recesses 12 through the slits 5 of the base substrate 1 from the recess 11A. In this process, the resin 7A supplied from the recess 11A to the recesses 12 through the slits 5 flows in the axial direction of the bonding wires 6, i.e., in the vertical direction, from one end side of the bonding wires 6. This vertical flow of resin prevents the bonding wires 6 from being deformed whereas the resin flow along the surface of the base substrate 1, i.e., in the lateral direction may deform them.

[0061]

Then, the base substrate 1 is taken out of the molding die 10, and bump electrodes 4 are electrically and mechanically connected to the surfaces of the first electrode pads 1B on the back of the base substrate 1. Thus, a nearly completed semiconductor device shown in FIG. 1, 2, 3 and 4 is obtained.

[0062]

After this, the semiconductor device is shipped as a product. The semiconductor device shipped as a product is mounted on a mounting surface of a mounting board 15, with the bump electrodes 4 of the semiconductor device electrically and mechanically connected to electrode pads 15A arranged on the mounting surface of the mounting board

15, as shown in FIG. 10 (cross section). The connection between the bump electrodes 4 of the semiconductor device and the electrode pads 15A of the mounting board 15, although it depends on the material of the bump electrodes 4, may be accomplished in an atmosphere at a reflow temperature of, for instance, around 210°C to 230°C. In this mounting process, because the base substrate 1 is made from a rigid substrate which has a smaller thermal expansion coefficient in the planar direction and a higher Young's modulus, which means it is more difficult to bend than a flexible substrate, the base substrate 1 can be prevented from being deformed due to reflow heat.

[0063]

This embodiment offers the following advantages.

[0064]

(1) A semiconductor device comprises a semiconductor pellet 2 mounted on a pellet mounting area of the main surface of a base substrate 1, in which first electrode pads 1B arranged on the back of the base substrate 1 are electrically connected to external terminals 2A arranged on the main surface of the semiconductor pellet 2. The base substrate 1 is formed of a rigid substrate, and its first electrode pads 1B are electrically connected to the second electrode pads 1A arranged on its reverse side. The semiconductor pellet 2 is mounted on the pellet

mounting area of the main surface of the base substrate 1, with its main surface downward, and its external terminals 2A are electrically connected with the second electrode pads 1A of the base substrate 1 through bonding wires 6 passing through slits 5 formed in the base substrate 1. Because with this construction the external terminals 2A of the semiconductor pellet 2 and the first electrode pads 1B of the base substrate 1 can be electrically interconnected through the bonding wires 6 and second electrode pads 1A, it is possible to eliminate the through hole conductors used to electrically connect the second electrode pads 1A with the first electrode pads 1B. This in turn allows the base substrate 1 to be reduced in size by an amount corresponding to the occupied area of the through hole conductors (area of land region), which contributes to size reduction of the semiconductor device.

[0065]

Because the first electrode pads 1B can be put closer to the second electrode pads 1A by a distance corresponding to the occupied area of the through hole conductors, it is possible to shorten the length of the conductors 1B, of the base substrate 1 that electrically connect the second electrode pads 1A with the first electrode pads 1B. This reduces the inductance, increasing the operation speed of the semiconductor device.

[0066]

Further, because the rigid substrate has a higher Young's modulus and is harder than the flexible substrate of the conventional structure, the bonding force applied to the second electrode pads 1A is not absorbed by the base substrate 1 when electrically connecting the external terminals 2A on the main surface of the semiconductor pellet 2 and the second electrode pads 1A on the back of the base substrate 1 by the bonding wires 6. As a result, the bonding force and the ultrasonic vibrations are effectively transferred to the second electrode pads 1A. This in turn increases the connection strength between the bonding wires 6 and the second electrode pads 1A, preventing possible connection failures of the bonding wires 6, enhancing the electric reliability of the semiconductor device.

[0067]

Moreover, because the rigid substrate has a smaller thermal expansion coefficient in the planar direction and a higher Young's modulus than a flexible substrate, which means it is more resistant to bending, the base substrate 1 is free from deformations (warping and twisting) due to reflow heat when the semiconductor device is mounted on the mounting surface of the mounting board 15. As a result, a sufficient degree of flatness of the back of the

base substrate 1 with respect to the mounting surface of the mounting board 15 can be secured, enhancing the mounting precision of the semiconductor device.

[0068]

Further, because the rigid substrate has a smaller thermal expansion coefficient in the planar direction and a higher Young's modulus than that of the flexible substrate, which means it is more resistant to bending, the warping of the base substrate 1 can be limited to less than 100 μm even when the external size of the base substrate 1 increases in accordance with the number of the first electrode pads 1B increased.

[0069]

With the warping of the base substrate 1 limited to within 100 μm, it is possible to eliminate a reinforcement substrate intended to prevent warping of the base substrate 1. This reduces the manufacture cost of the semiconductor device compared with that of a semiconductor device having a reinforcement substrate.

[0070]

Furthermore, because the base substrate 1 can be formed of a printed wiring board of a single layer structure having the second electrode pads 1A, first electrode pads 1B and conductors 1B, respectively arranged on the back of a rigid substrate, the parts cost of the

base substrate 1 can be reduced compared with that of a base substrate formed of a two-layer printed wiring board formed on both the main and back surfaces of the rigid substrate. This means that the overall cost of manufacturing semiconductor devices can be lowered.

[0071]

(2) The slits 5 extends in the directions of rows of external terminals 2A arranged on the main surface of the semiconductor pellet 2 and are located at positions over the external terminals 2A of the semiconductor pellet 2. With this construction, the slits 5 are arranged within the area occupied by the semiconductor pellet 2, so that the base substrate 1 corresponding to the area occupied by the slits 5 can not increase in size.

[0072]

(3) The second electrode pads 1A are arranged in two opposite areas of the back of the base substrate 1 divided by the slits 5. This construction allows an increase in the number of power supply paths for electrically connecting the external terminals 2A of the semiconductor pellet 2 with the second electrode pads 1A of the base substrate 1. This in turn makes it possible to reduce power supply noise generated at time of simultaneous switching of signals, thereby preventing malfunctions of the semiconductor device.

[0073]

Further, even when the pitch of the second electrode pads 1A of the base substrate 1 is set larger than that of the external terminals 2A of the semiconductor pellet 2, the length of the row of the second electrode pads 1A for each side of the semiconductor pellet 2 can be made almost equal to the length of the row of the external terminals 2A for each side of the semiconductor pellet 2. This prevents an increase in the length of the bonding wires 6, which is dependent on the length of the row of the second electrode pads 1A. As a result, it is possible to prevent the bonding wires 6 from being deformed by the flow of resin when the bonding wires 6 are sealed by the resin sealing body 7 according to the transfer molding.

[0074]

Further, because the second electrode pads 1A can be located at positions on the base substrate 1 facing the external terminals 2A of the semiconductor pellet 2, the lengths of the bonding wires 6 can be made uniform, which in turn makes uniform the inductances of the signal paths between the external terminals 2A of the semiconductor pellet 2 and the second electrode pads 1A of the base substrate 1.

[0075]

(4) The back of the semiconductor pellet 2 opposing

its main surface is exposed from the resin sealing body 7 that covers the peripheral area around the main surface of the base substrate 1. This structure allows the heat generated by the operation of the circuit system mounted on the semiconductor pellet 2 to be released from the back of the semiconductor pellet 2 to outside, thus enhancing the heat dissipation efficiency of the semiconductor device.

[0076]

Further, because the mechanical strength of the base substrate 1 can be reinforced by the mechanical strength of the resin sealing body 7, deformations of the base substrate 1 (warping and twisting) due to reflow heat during mounting can be prevented.

[0077]

(5) The bonding wires 6 are sealed with the resin sealing body 7. This structure prevents the bonding wires 6 from being deformed due to external impacts and contacts, thus enhancing the electric reliability of the semiconductor device.

[0078]

(6) The resin sealing body 7 is formed both on the main surface side and the back surface side of the base substrate 1. This structure prevents the resin sealing body 7 from becoming separated from the base substrate 1

due to the thermal stresses generated during a temperature cycle test or when the bump electrodes 4 are connected. This in turn enhances the reliability of the semiconductor device.

[0079]

(7) A method of manufacturing a semiconductor device, in which a semiconductor pellet 2 is mounted on a pellet mounting area of the main surface of a base substrate 1 and in which first electrode pads 1B arranged on the back of the base substrate 1 are electrically connected to external terminals 2A arranged on the main surface of the semiconductor pellet 2, comprises a step of mounting the semiconductor pellet 2, with its main surface downward, on the pellet mounting area of the main surface of the base substrate 1 formed of a rigid substrate, and a step of electrically connecting the external terminals 2A of the semiconductor pellet 2 to the second electrode pads 1A, which are electrically connected to the first electrode pads 1B of the base substrate 1 and arranged on the back of the base substrate 1, through bonding wires 6 passing through slits 5 formed in the base substrate 1. The external terminals 2A of the semiconductor pellet 2 and the first electrode pads 1B of the base substrate 1 therefore are electrically connected through the bonding wires 6 and the second electrode pads 1A, so that through

hole conductors 1C used for electrically connecting the second electrode pads 1A with the first electrode pads 1B can be eliminated, reducing the external size of the base substrate 1 by an amount corresponding to the occupied area of the through hole conductors. As a result, the overall external size of the semiconductor device can be reduced.

[0080]

Further, because the external pads 2A of the semiconductor pellet 2 and the first electrode pads 1B of the base substrate 1 are electrically connected through the bonding wires 6 and the second electrode pads 1A, there is no need for through hole conductors 1C to electrically connect the second electrode pads 1A with the first electrode pads 1B. This makes it possible to use a base substrate 1 in which the conductors 1B, electrically connecting the second electrode pads 1A with the first electrode pads 1B are shorter by a length corresponding to the occupied area of the through hole conductors. As a result, it is possible to fabricate a semiconductor device with fast operating speeds.

[0081]

Because the base substrate 1 used is formed of a rigid substrate having a higher Young's modulus, which means it is harder than a flexible substrate, the bonding

force applied to the second electrode pads 1A when electrically connecting the external terminals 2A arranged on the main surface of the semiconductor pellet 2 with the second electrode pads 1A arranged on the back of the base substrate 1 through the bonding wires 6 is not absorbed by the base substrate 1, effectively transmitting the bonding force and ultrasonic vibrations to the second electrode pads 1A. As a result, the connection strength between the bonding wires 6 and the second electrode pads 1A can be increased, which in turn allows the manufacture of a semiconductor device with high electric reliability.

[0082]

Because the base substrate 1 is formed of a rigid substrate having a smaller thermal expansion coefficient in the planar direction and a higher Young's modulus, which means it is more resistant to bending than a flexible substrate, the base substrate 1 is prevented from being deformed (warped or twisted) due to reflow heat during the process of mounting the semiconductor device on the mounting surface of the mounting board 15. This allows the back surface of the base substrate 1 to have a sufficient degree of flatness with respect to the mounting surface of the mounting board 15, thus enhancing the mounting precision of the semiconductor device.

[0083]

(8) Following the process of electrically connecting with the bonding wires 6, the method of manufacture includes a process of transfer molding of a resin sealing body 7 that covers the peripheral area of the main surface of the base substrate 1 and seals the bonding wires 6. Because the base substrate 1 uses a rigid substrate which has a smaller thermal expansion coefficient in the planar direction and a higher Young's modulus and is more resistant to bending than a flexible substrate, this method prevents the base substrate 1 from being deformed (warped or twisted) due to heating of the molding die 10.

[0084]

Because the resin 7A supplied from the recess 11A into the recesses 12 through the slits 5 flows from one end side of the bonding wires 6 in their axial direction, i.e., in the vertical direction, the bonding wires 6 are not deformed by the flow of the resin 7A, whereas they can be deformed when the resin flows along the surface of the base substrate 1, i.e., in the lateral direction.

[0085]

As shown in FIG. 11 (cross section), the resin sealing body 7 may be formed on the back surface of the base substrate 1 excluding the surfaces of the second electrode pads 1A and first electrode pads 1B. In this case, the base substrate 1 is held and clamped by the

resin sealing body 7 and therefore prevented from being warped.

[0086]

The base substrate 1 may, though not shown, be formed in a multilayer structure in which a plurality of rigid substrates is stacked together. This structure can reduce the manufacture cost of the semiconductor device as compared with a base substrate made up of a plurality of flexible substrates stacked together.

[0087]

(Embodiment 2)

The outline configuration of a semiconductor device as the second embodiment of this invention that employs a BGA structure is shown in FIG. 12 (cross section) and FIG. 13 (enlarged plan view of an essential part of the back side showing the state of the back side with the resin sealing body removed).

[0088]

As shown in FIGS. 12 and 13, the semiconductor device has the semiconductor pellet 2 mounted by a facedown system on the pellet mounting area of the main surface of the base substrate 1 with an insulating layer 3 disposed therebetween. A plurality of bump electrodes 4 are arranged in grid on the back of the base substrate 1.

[0089]

Arranged in the central area of the main surface of the semiconductor pellet 2 is a plurality of external terminals 2A along the longer sides thereof, which are electrically connected to the second electrode pads 1A arranged on the back of the base substrate 1 through the bonding wires 6 passing through the slits 5 formed in the base substrate 1. A plurality of second electrode pads 1A are electrically connected to the corresponding first electrode pads 1B arranged on the back of the base substrate 1 through conductors 1B₁. Bump electrodes 4 are electrically and mechanically connected to the surfaces of the first electrode pads 1B. That is, the external terminals 2A of the semiconductor pellet 2 are electrically connected to the first electrode pads 1B through the bonding wires 6, second electrode pads 1A and conductors 1B₁.

[0090]

The slits 5 of the base substrate 1 are formed in the central area of the main surface of the semiconductor pellet 2 along the direction of the row of the external terminals 2A arranged along the longer side of the semiconductor pellet 2. The slits 5 are tapered so that its opening size on the back side of the base substrate 1 is greater than the opening size on the main surface side.

[0091]

As described above, this embodiment offers similar effects and advantages to those of the first embodiment. With the slits 5 tapered, it is possible to prevent contact between the base substrate 1 and a bonding tool when one end of the bonding wires 6 is bonded to the external terminals 2A of the semiconductor pellet 2. This in turn raises the yield of semiconductor device assembly in the bonding process.

[0092]

(Embodiment 3)

The outline configuration of a semiconductor device as the third embodiment of this invention that employs a BGA structure is shown in FIG. 14 (a plan view of an essential part of the back side showing the state of the back side with the resin sealing body removed).

[0093]

As shown in FIG. 14, the semiconductor device has a semiconductor pellet 2 mounted facedown on a pellet mounting area of the main surface of the base substrate 1, with an insulating layer 3 disposed therebetween. Bump electrodes 4 are arranged in grid on the back of the base substrate 1.

[0094]

At the outer periphery of the main surface of the semiconductor pellet 2, a plurality of external terminals

2A are arranged along the sides of the pellet. At the central portion of the main surface of the semiconductor pellet 2, a plurality of external terminals 2A are arranged along the longer or shorter side of the pellet. The external terminals 2A are electrically connected to the second electrode pads 1A arranged on the back of the base substrate 1 by bonding wires 6 passing through slits 5 formed in the base substrate 1. Each of a plurality of the second electrode pads 1A is electrically connected to each of a plurality of the first electrode pads 1B arranged on the back of the base substrate 1 through conductors 1B₁. Bump electrodes 4 are electrically and mechanically connected to the surfaces of the individual first electrode pads 1B. That is, the external terminals 2A of the semiconductor pellet 2 are electrically connected to the first electrode pads 1B through the bonding wires 6, second electrode pads 1A and conductors 1B₁.

[0095]

The slits 5 of the base substrate 1 are arranged at each sides of the semiconductor pellet 2 and also at the central portion of the semiconductor pellet 2. That is, the base substrate 1 of this embodiment has five slits 5, each of which is located above the external terminals 2A of the semiconductor pellet 2.

[0096]

As explained above, this embodiment offers the similar effects and advantages to those of the first embodiment. Because the slits 5 are arranged at the sides and the central portion of the semiconductor pellet 2, it is possible to increase the number of external terminals 2A arranged on the main surface of the semiconductor pellet 2 and the number of second electrode pads 1A arranged on the back of the base substrate 1. This allows an increase in the number of power supply paths for electrically connecting the external terminals 2A of the semiconductor pellet 2 with the second electrode pads 1A of the base substrate 1. This in turn allows a further reduction in power supply noise generated when output signals are switched simultaneously. Furthermore, this construction makes it possible to increase the number of signal paths electrically connecting the external terminals 2A of the semiconductor pellet 2 with the second electrode pads 1A of the base substrate 1 and therefore reduce the external size of the semiconductor pellet 2 limited by the number of external terminals 2A.

[0097]

Although this embodiment has been shown to have only one slit 5 formed at the central portion of the semiconductor pellet 2, two or more slits 5 may be

arranged parallel or crosswise to each other at the central part of the semiconductor pellet 2. By increasing the number of slits 5 in this way, it is possible to further increase the number of the second electrode pads 1A of the base substrate 1 and the number of the external terminals 2A of the semiconductor pellet 2.

[0098]

(Embodiment 4)

The outline configuration of a semiconductor device as the fourth embodiment of this invention that employs a BGA structure is shown in FIG. 15 (plan view of an essential part of the back side showing the state of the back side with the resin sealing body removed).

[0099]

As shown in FIG. 15, the semiconductor device has a semiconductor pellet 2 mounted by a facedown system on a pellet mounting area of the main surface of the base substrate 1, with an insulating layer 3 disposed therebetween. Bump electrodes 4 are arranged in grid on the back of the base substrate 1. The base substrate 1 is formed of a printed circuit board of, for example, 3-layer wiring structure.

[0100]

At the outer periphery of the main surface of the semiconductor pellet 2, a plurality of external terminals

2A are arranged along the sides of the pellet. Each of the plurality of external terminals 2A is electrically connected to each of a plurality of the second electrode pads 1A arranged on the back of the base substrate 1 through bonding wires 6 passing through slits 5 formed in the base substrate 1.

[0101]

Of the second electrode pads 1A, electrode pads 1A₂ are formed integral with electrode plates 8A. The electrode plates 8A are electrically connected to other electrode plates 8A via through hole conductors (not shown) and internal wiring (not shown) in the base substrate 1. A reference voltage (0 V for example) as a power supply is applied to the electrode plates 8A. Of the second electrode pads 1A, electrode pads 1A₃ are formed integrally with an electrode plate 8B. An operating voltage (3.3 V for instance) as a power supply is applied to this electrode plate 8A.

[0102]

With this embodiment, because the through hole conductors 1C that electrically connect the second electrode pads 1A on the main surface of the base substrate 1 and the first electrode pads 1B on the back are eliminated, the electrode plates 8A and the electrode plate 8B can be arranged on the back of the base substrate

1. This allows the bump electrodes 4 to be freely located and shortens the distance between the external terminals 2A of the semiconductor pellet 2 and the pump electrodes 4. As a result, the inductance can be reduced, thereby increasing the operating speeds of the semiconductor device.

[0103]

The invention has been described in detail in connection with representative embodiments of the invention. It is noted, however, that the invention is not limited to these embodiments but that many modifications may be made without departing from the spirit of the invention.

[0104]

[Effects of the Invention]

Representative advantages of this invention disclosed by this application may be summarized as follows.

[0105]

It is possible to reduce the size of a semiconductor device in which the semiconductor pellet is mounted on the pellet mounting area of the main surface of the base substrate and in which the first electrode pads arranged on the back of the base substrate are electrically connected to the external terminals arranged on the main surface of the semiconductor pellet.

[0106]

It is possible to increase the operating speed of the semiconductor device.

[0107]

It is also possible to enhance the electric reliability of the semiconductor device.

[0108]

Further, it is possible to increase the mounting precision of the semiconductor device.

[Brief Description of the Drawings]

[FIG. 1]

FIG. 1 is a plan view of the main surface side of the semiconductor device which employs the BGA structure being Embodiment 1 of the present invention.

[FIG. 2]

FIG. 2 is a cross section taken along the line A-A of FIG. 1.

[FIG. 3]

FIG. 3 is an enlarged cross section of an essential part of FIG. 2.

[FIG. 4]

FIG. 4 is an enlarged plan view showing the back side of an essential part of the semiconductor device with the resin sealing body removed.

[FIG. 5]

FIG. 5 is a cross section of an essential part of a molding die which molds the resin sealing body of the semiconductor device.

[FIG. 6]

FIG. 6 is a cross section for explaining a manufacture method of the semiconductor device.

[FIG. 7]

FIG. 7 is a cross section of an essential part for explaining the manufacture method of the semiconductor device.

[FIG. 8]

FIG. 9 is another cross section of an essential part for explaining the manufacture method of the semiconductor device.

[FIG. 9]

FIG. 9 is a still another cross section of an essential part for explaining the manufacture method of the semiconductor device.

[FIG. 10]

FIG. 10 is a cross section showing a state in which the semiconductor device is mounted on a mounting board.

[FIG. 11]

FIG. 11 is a cross section showing a modification of the semiconductor device.

[FIG. 12]

FIG. 12 is a cross section of the semiconductor device which employs a BGA structure being Embodiment 2 of the present invention.

[FIG. 13]

FIG. 13 is an enlarged plan view of an essential part of the back side showing the state of the back side with the resin sealing body of the semiconductor device removed.

[FIG. 14]

FIG. 14 is a plan view of an essential part of the back side showing the state of the back side in which the resin sealing body in the semiconductor device which employs a BGA structure being Embodiment 3 of the present invention is removed.

[FIG. 15]

FIG. 15 is a plan view of an essential part of the back side showing the state of the back side in which the resin sealing body in the semiconductor device which employs a BGA structure being Embodiment 4 of the present invention is removed.

[FIG. 16]

FIG. 16 is a cross section of an essential part of the semiconductor device which employs a conventional BGA structure.

[Reference Numerals]

1: BASE SUBSTRATE
1A: ELECTRODE PAD
1B: ELECTRODE PAD
2: SEMICONDUCTOR PELLET
2A: EXTERNAL TERMINAL
3: INSULATING LAYER
4: BUMPER ELECTRODE
5: SLIT
6: BONDING WIRE
7: RESIN SEALING BODY
7A: RESIN
8A, 8B: ELECTRODE PLATE
10: MOLDING DIE
10A: UPPER DIE
10B: LOWER DIE
11: CAVITY
11A, 11B, 12: RECESS
13: INFLOW GATE
14: HEAT STAGE
14A: RECESS
15: MOUNTING BOARD
15A: ELECTRODE PAD

[Name of Document] ABSTRACT

[Abstract]

[Object] To provide a technology that allows a reduction in the size of the semiconductor device, a technology that allows an increase in the operating speed of the semiconductor device, a technology that can enhance electric reliability of the semiconductor device, and a technology that can enhance mounting precision of the semiconductor device.

[Solving Means] A semiconductor device comprising a semiconductor pellet 2 mounted on a pellet mounting area of the main surface of a base substrate 1, in which first electrode pads 1B arranged on the back of the base substrate 1 are electrically connected to external terminals 2A arranged on the main surface of the semiconductor pellet 2. The base substrate 1 is formed of a rigid substrate, and the first electrode pads 1B of the base substrate 1 are electrically connected to the second electrode pads 1A arranged on its reverse side. The semiconductor pellet 2 is mounted on the pellet mounting area of the main surface of the base substrate 1, with its main surface downward, and the external terminals 2A of the semiconductor pellet 2 are connected electrically with the second electrode pads 1A of the base substrate 1 through bonding wires 6 passing through slits 5 formed in

the base substrate 1.

[Selected Figure]

FIG. 3

FIG. 1

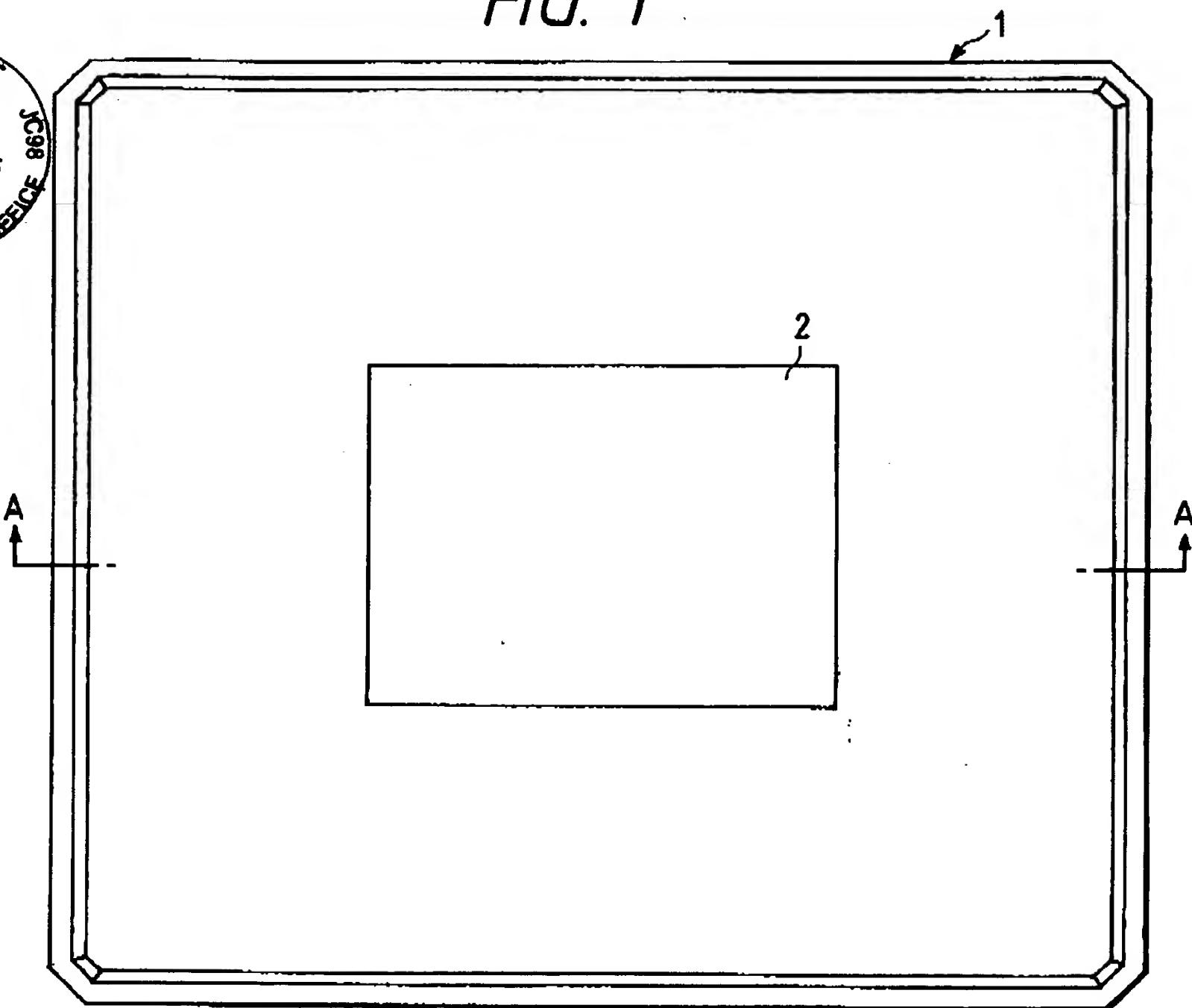


FIG. 2

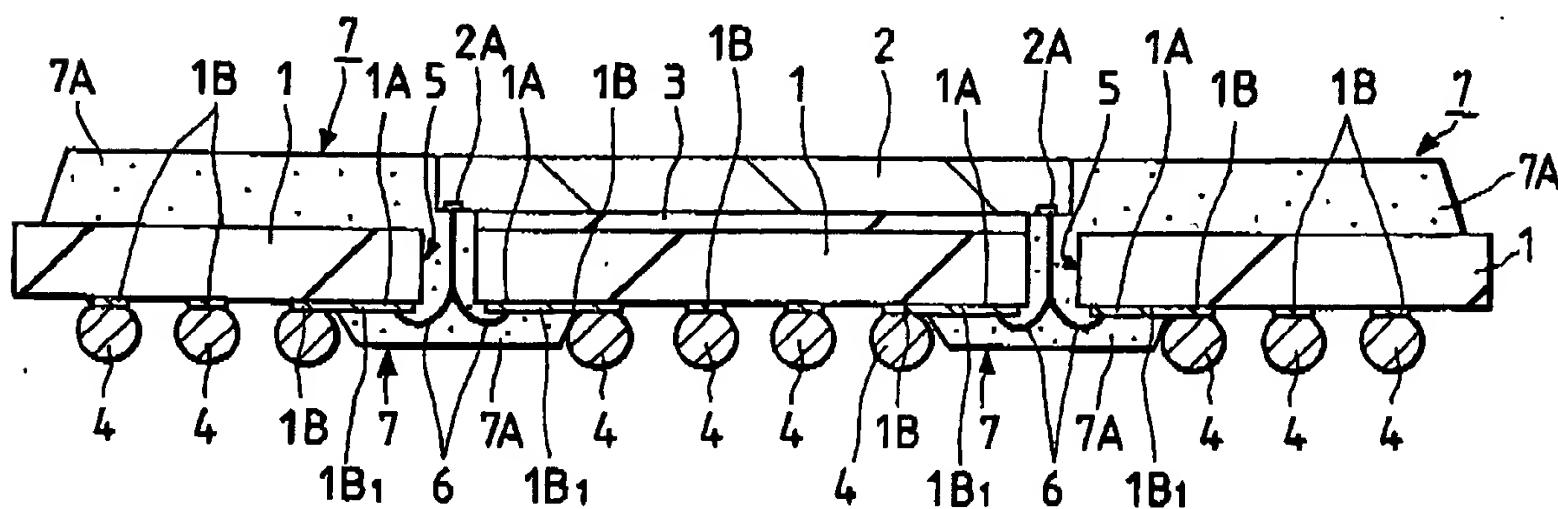


FIG. 3

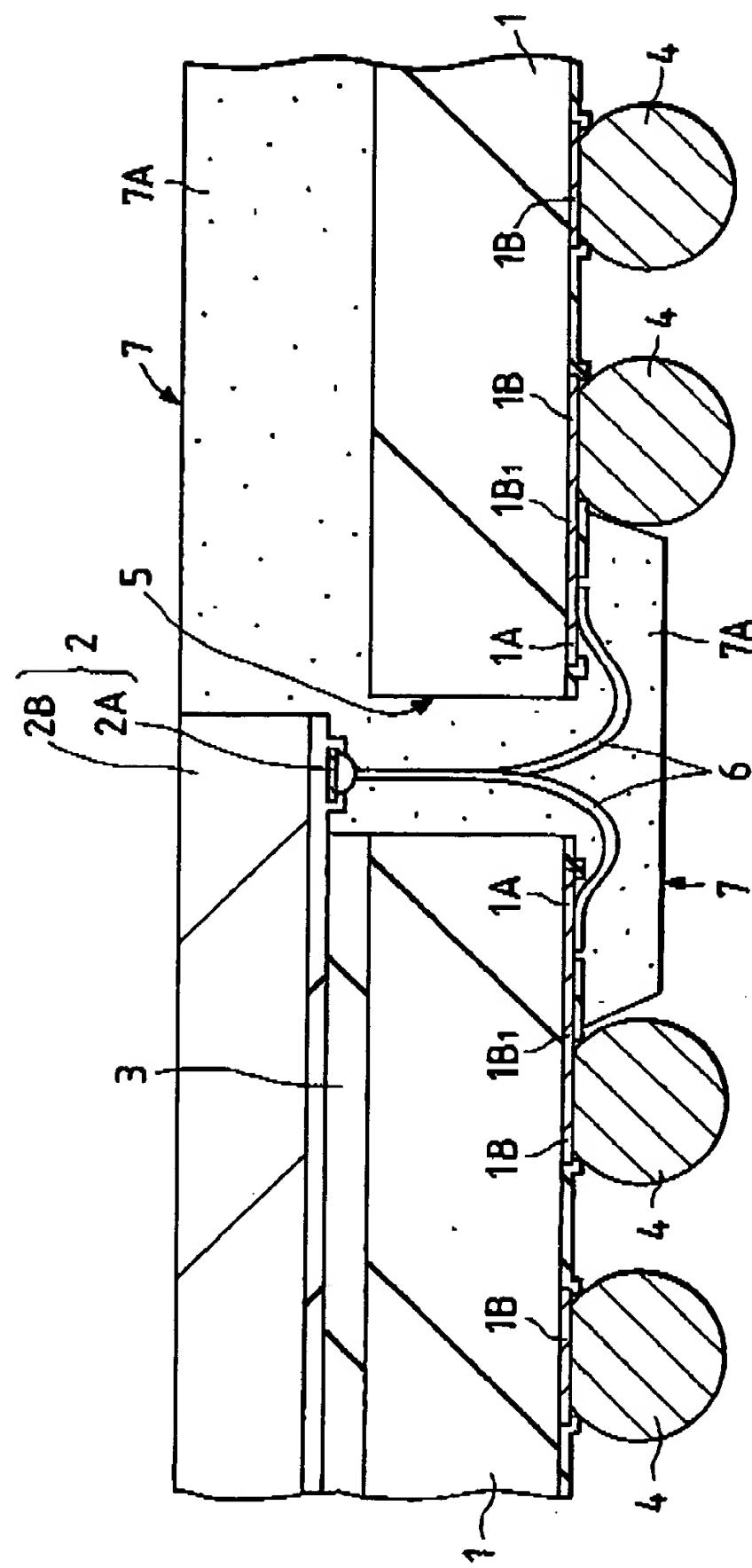


FIG. 4

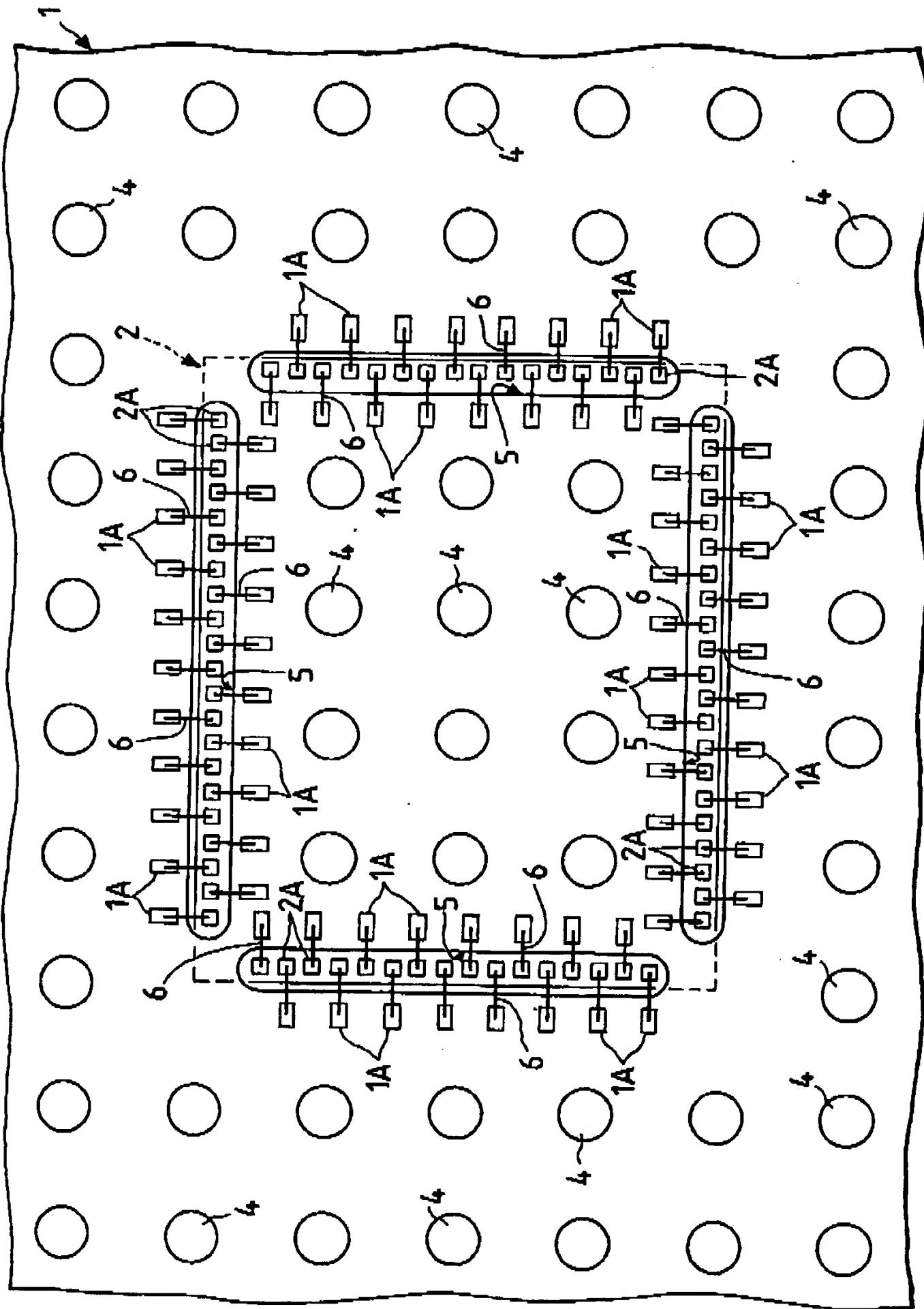


FIG. 5

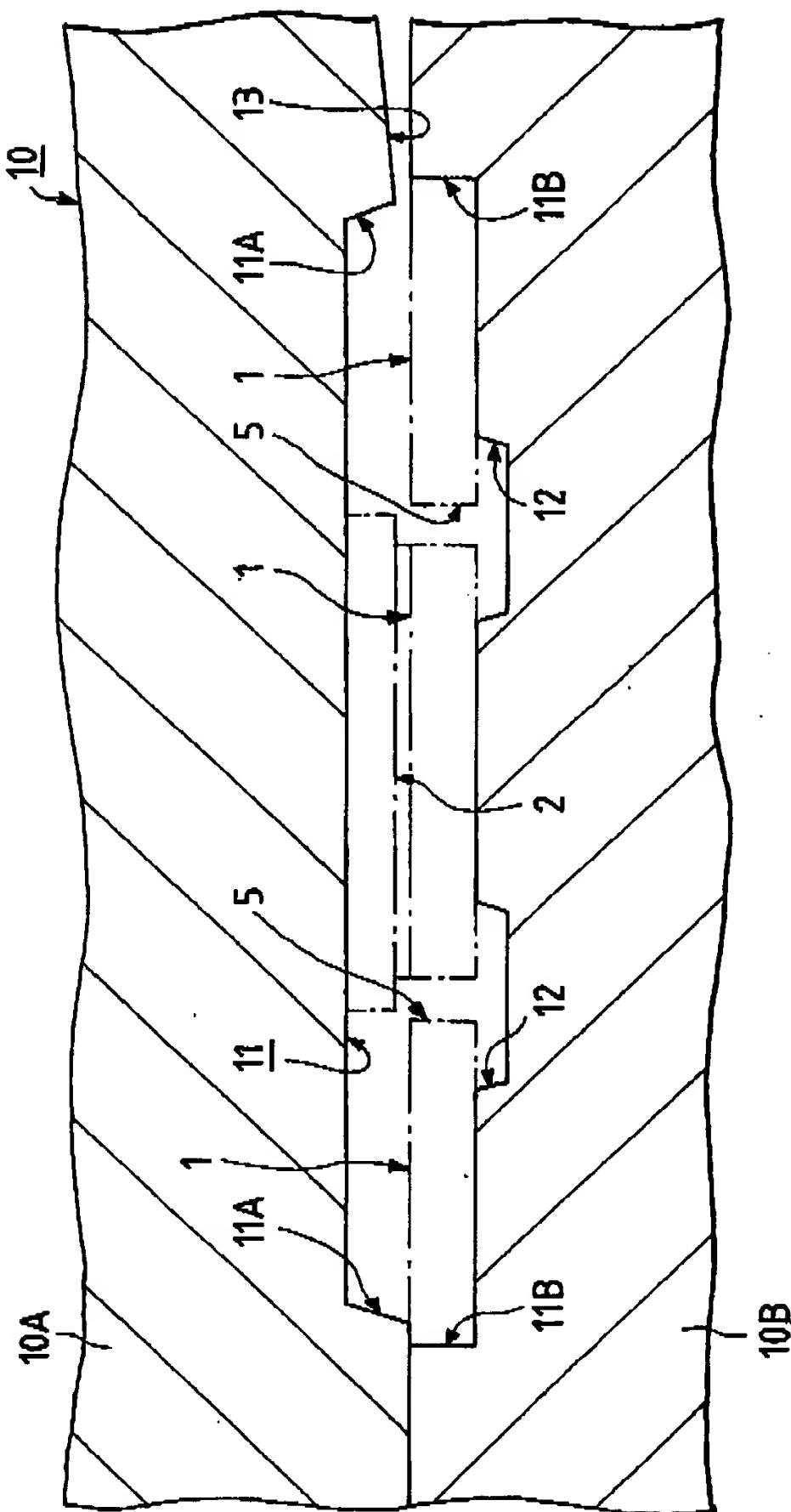


FIG. 6

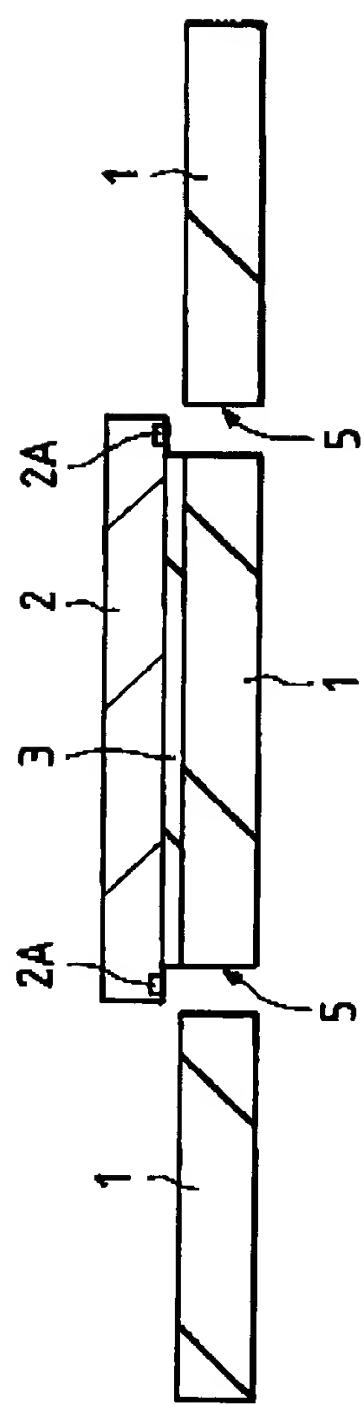


FIG. 7

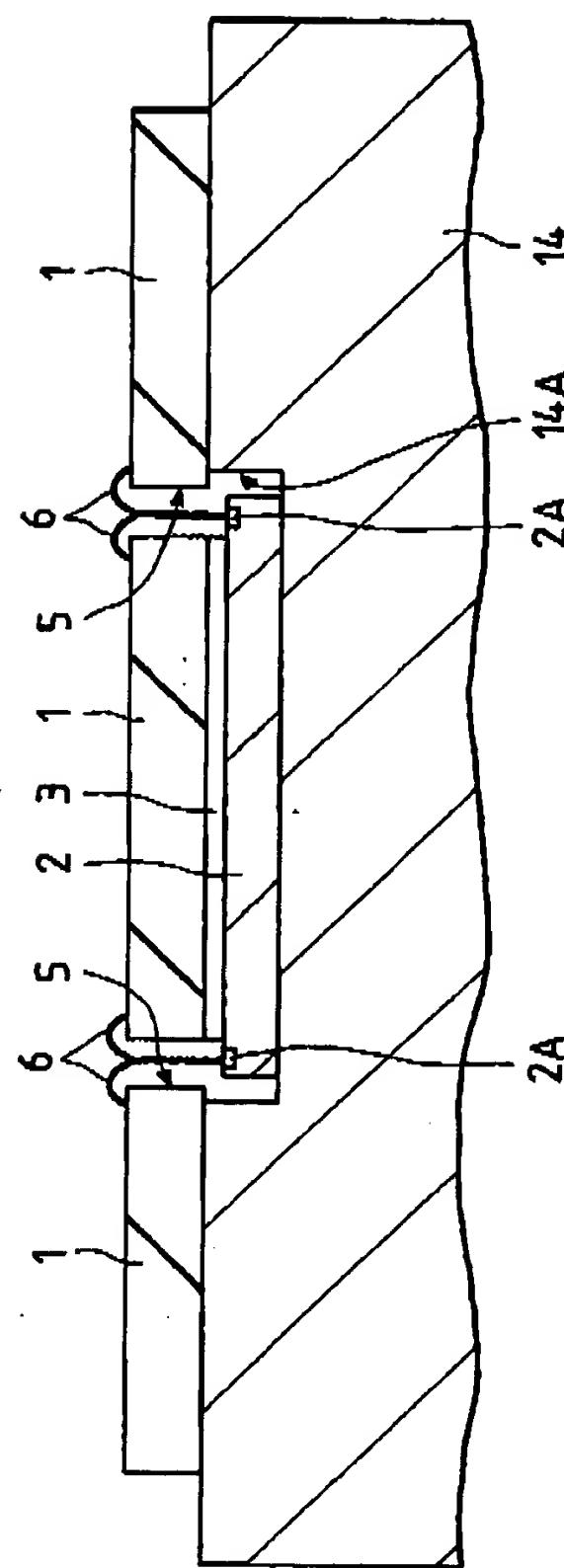
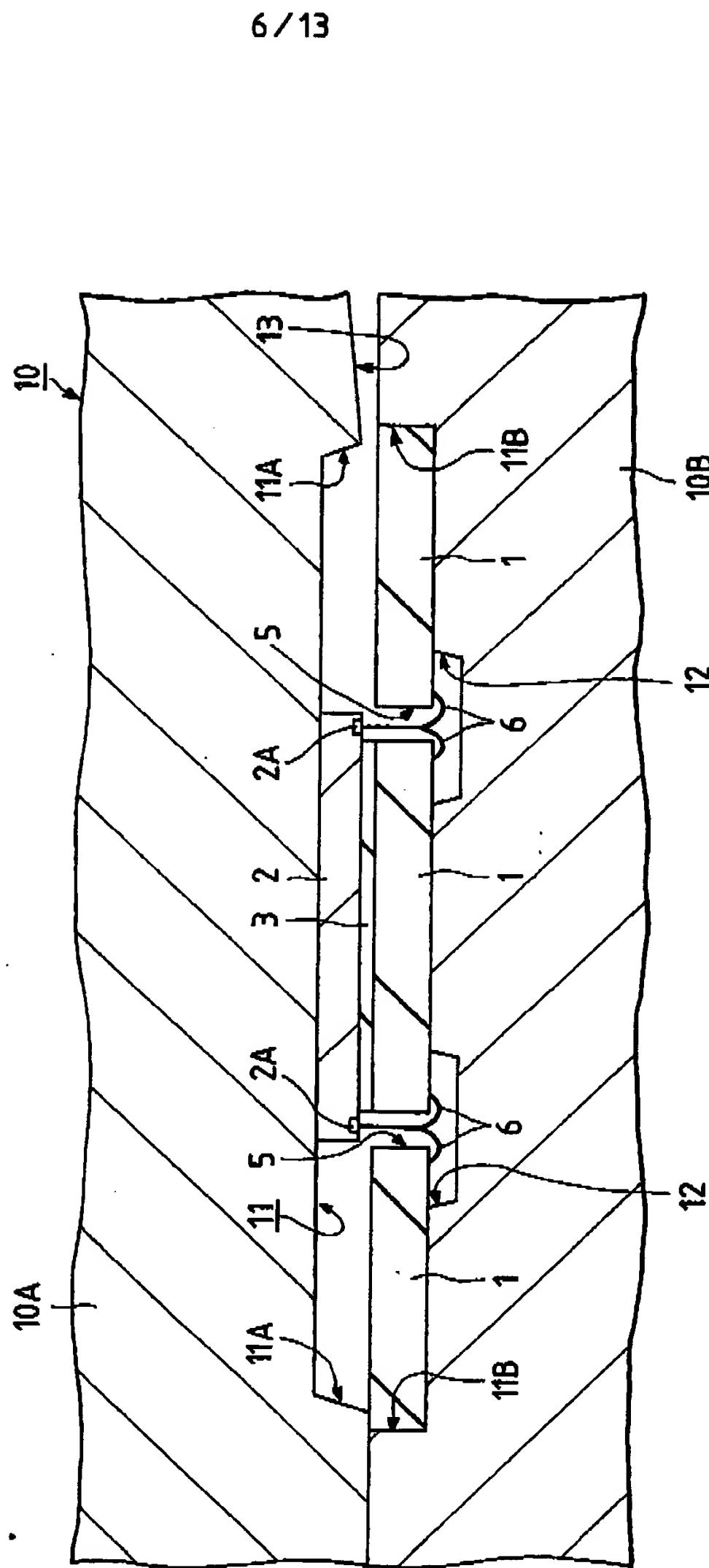


FIG. 8



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FIG. 9

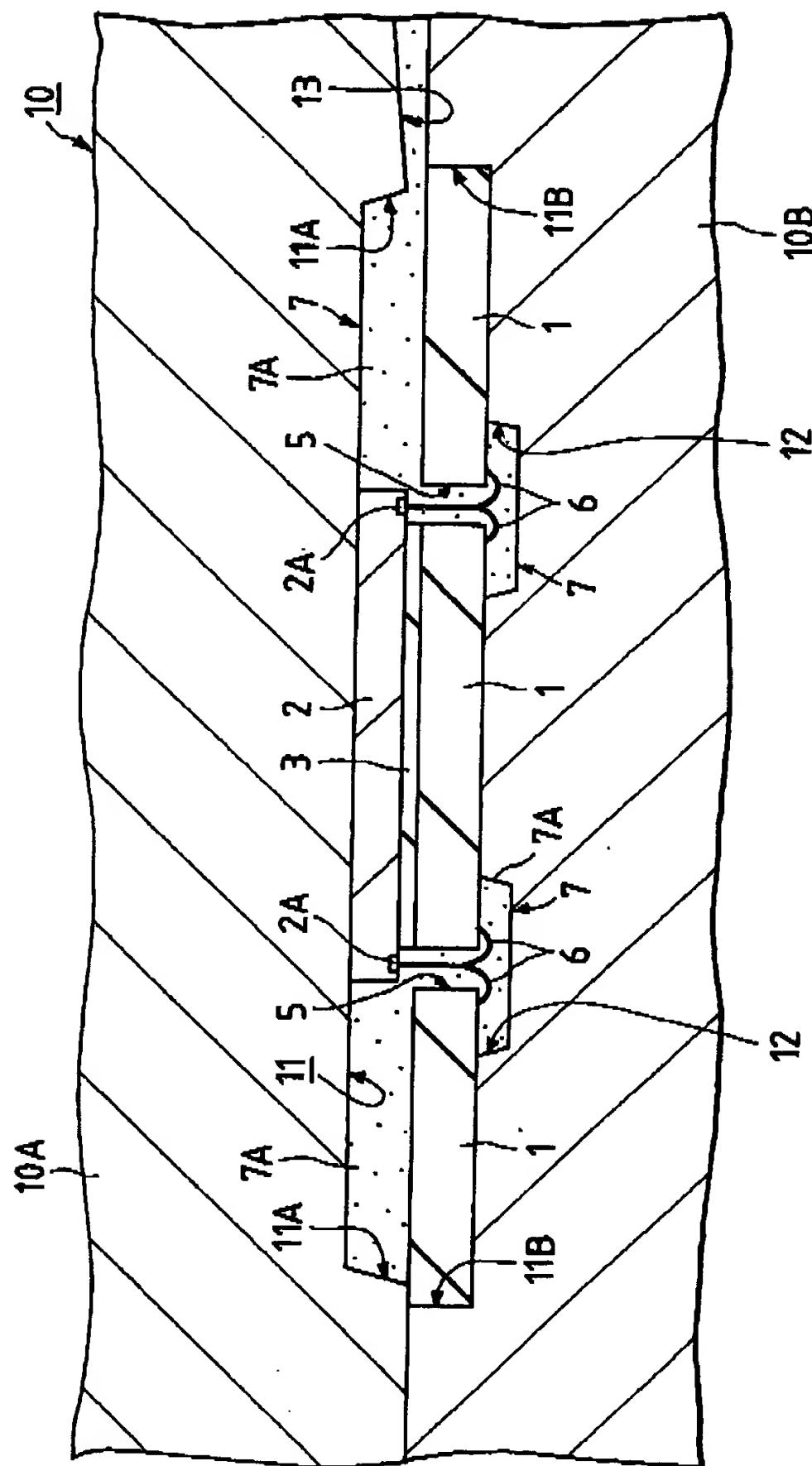


FIG. 10

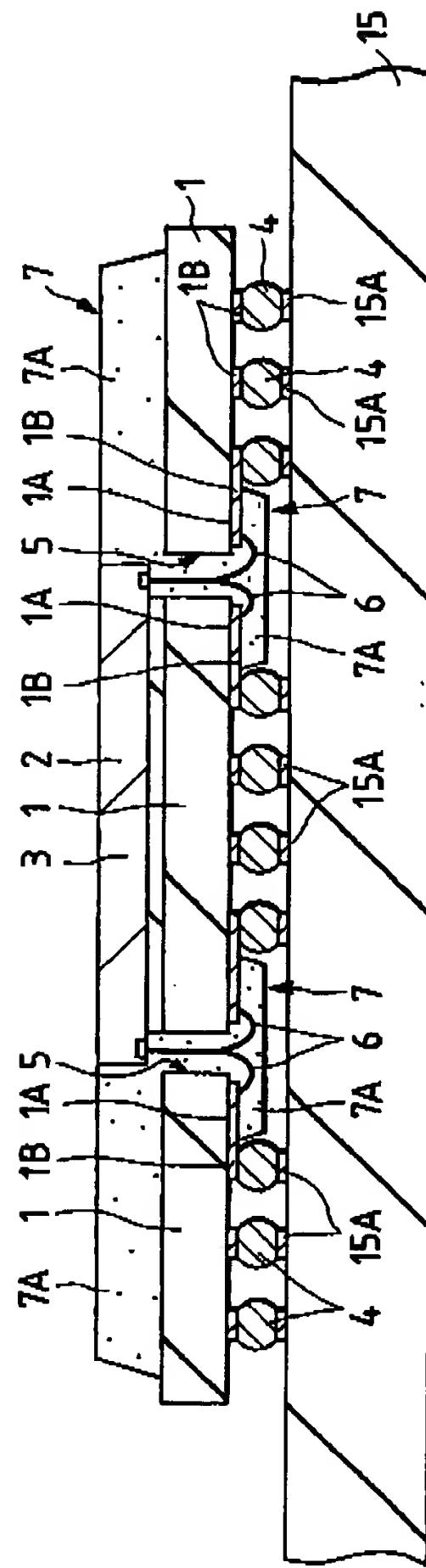
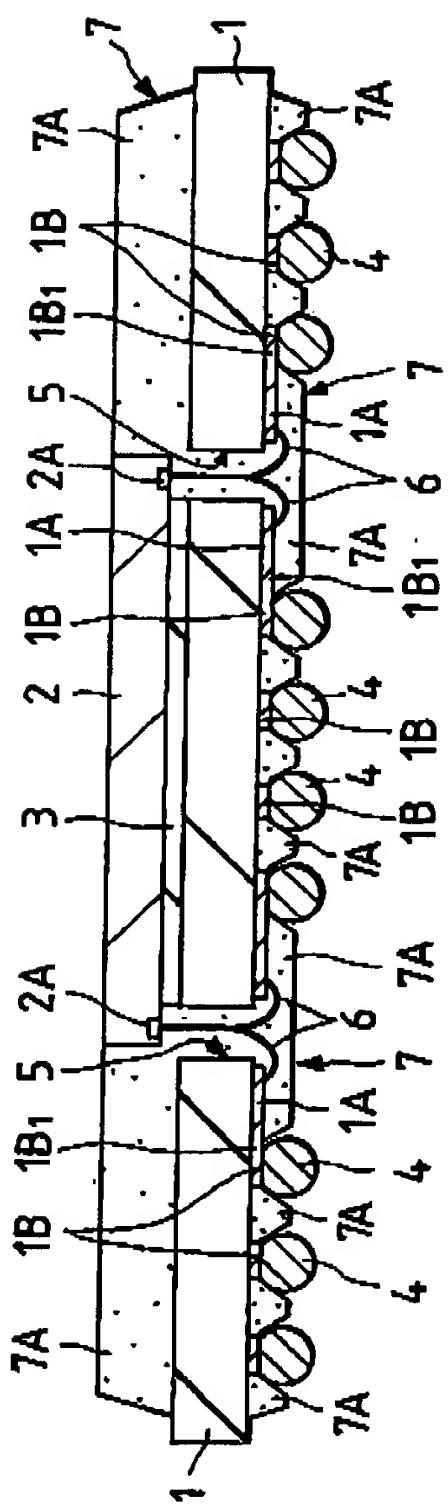


FIG. 11



E/G 12

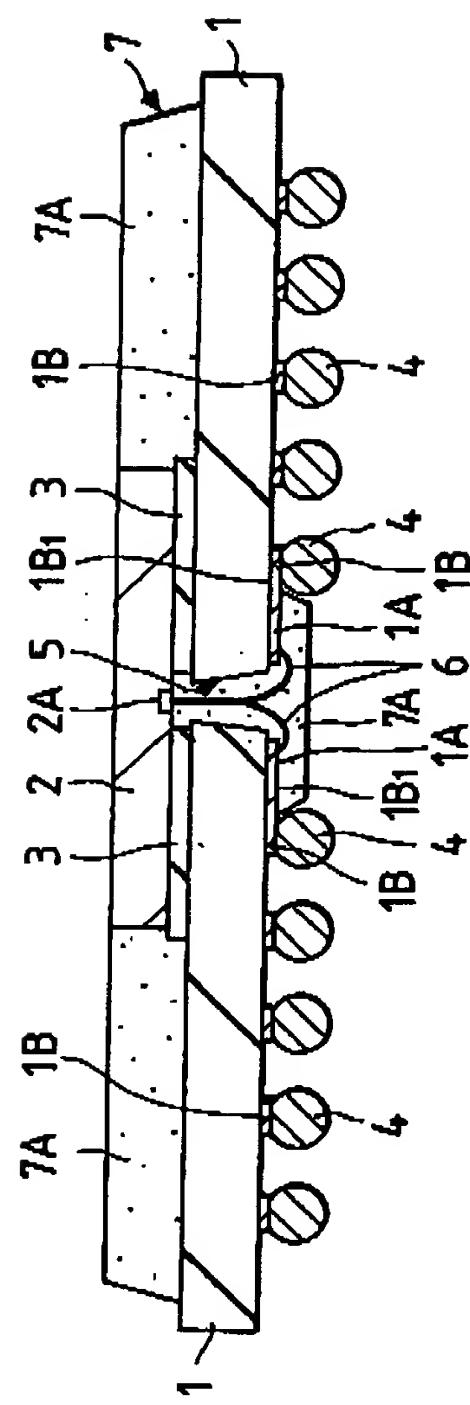


FIG. 13

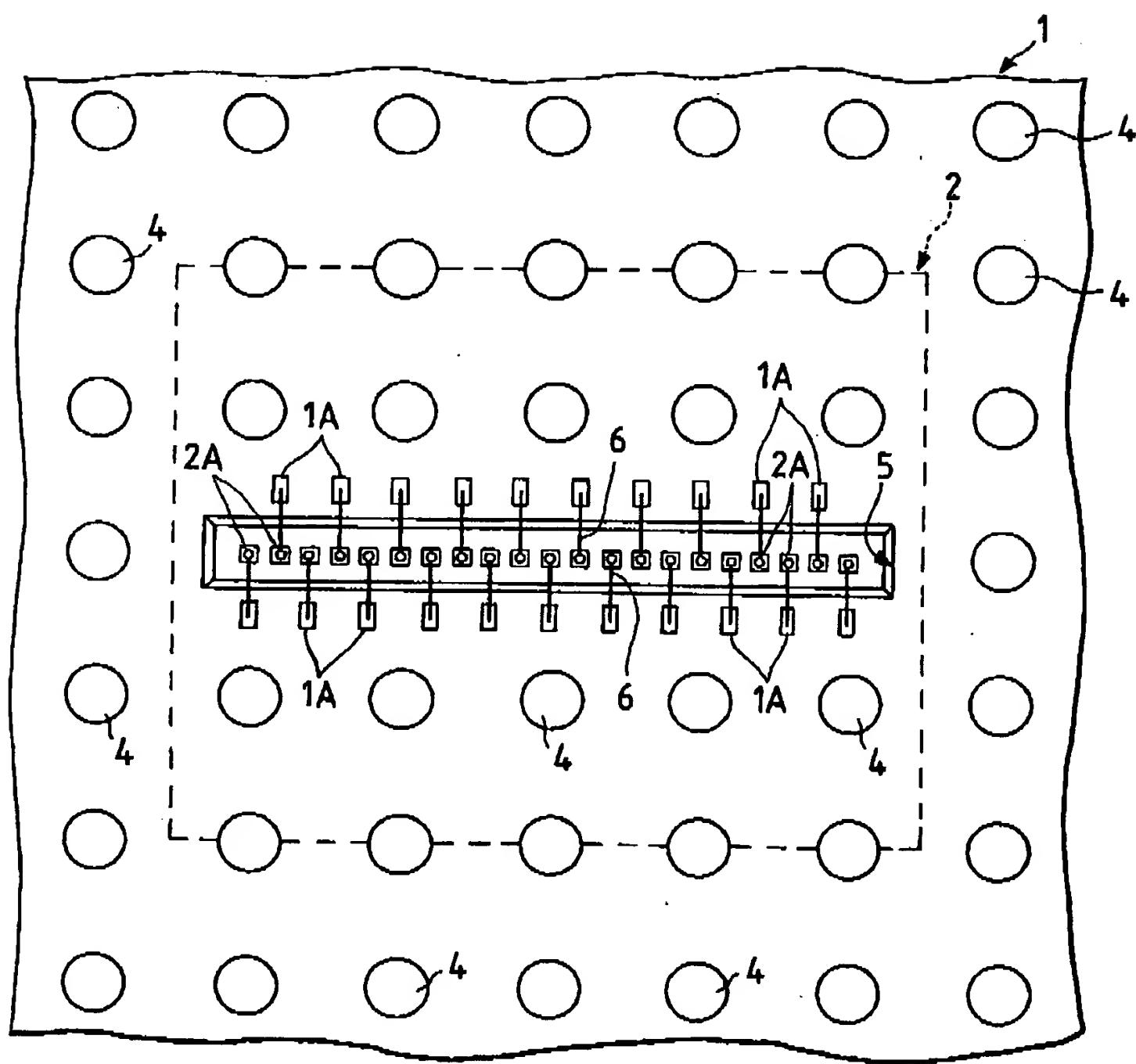


FIG. 14

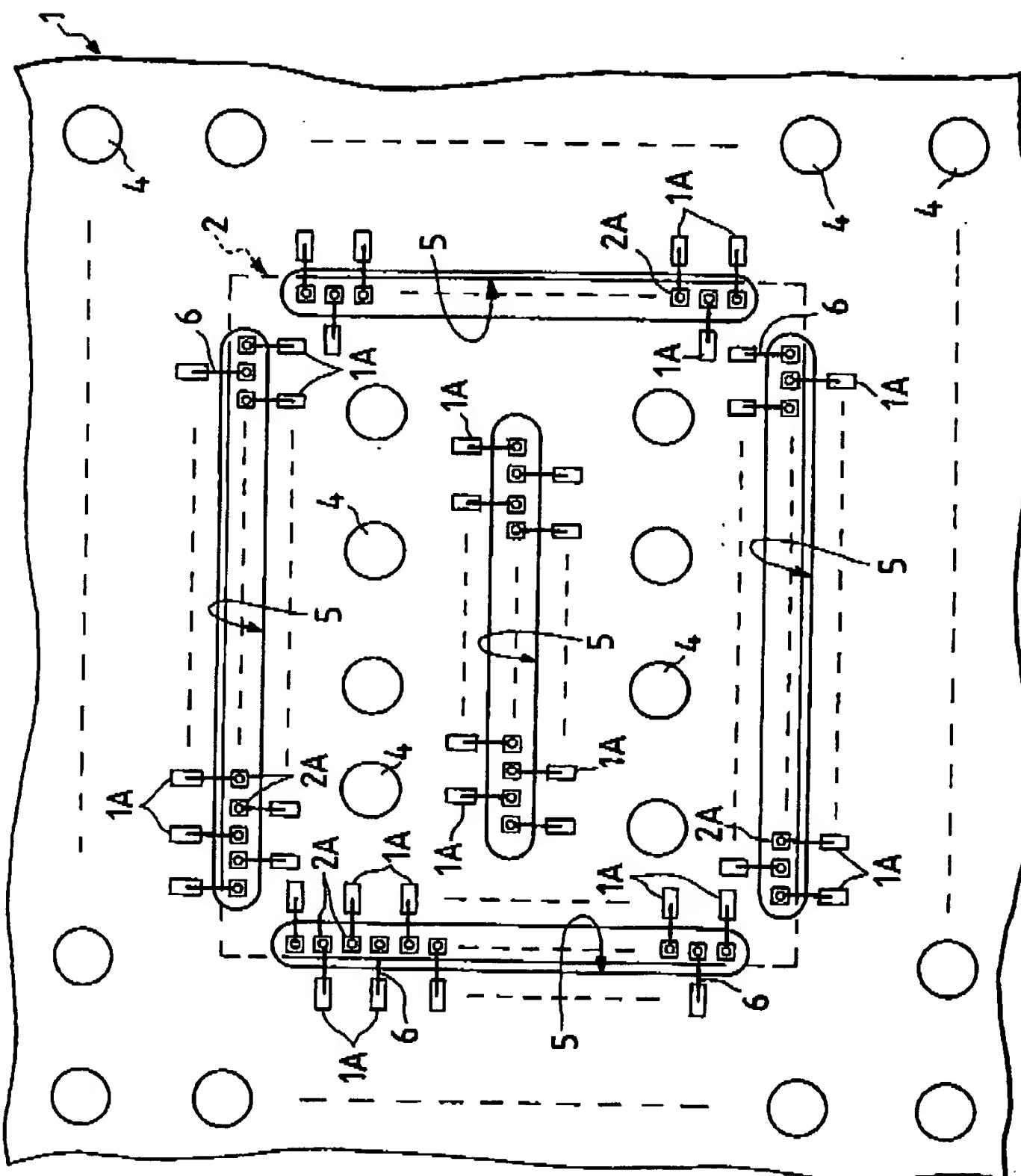


FIG. 15

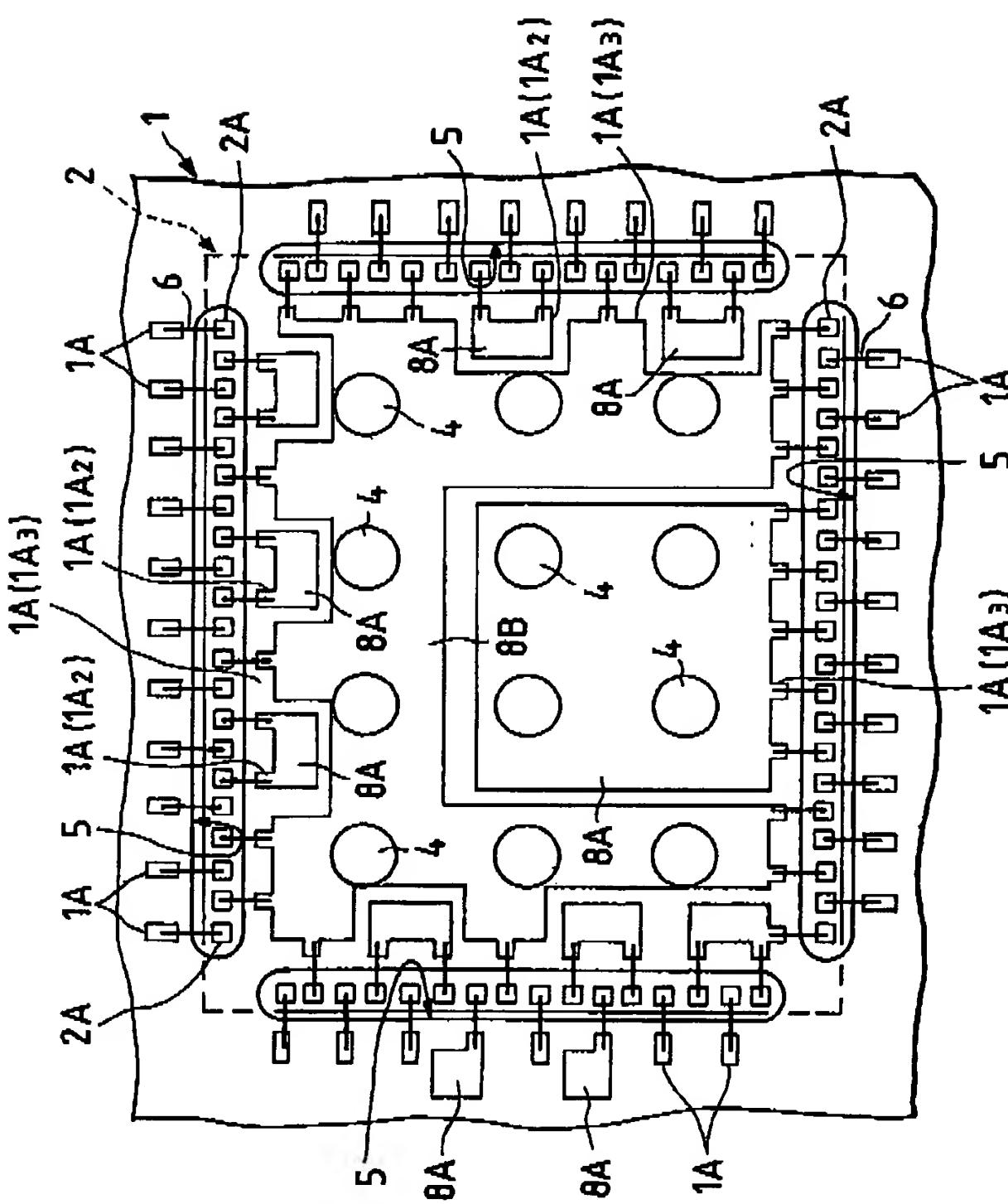


FIG. 16

